

Patent Abstracts of Japan

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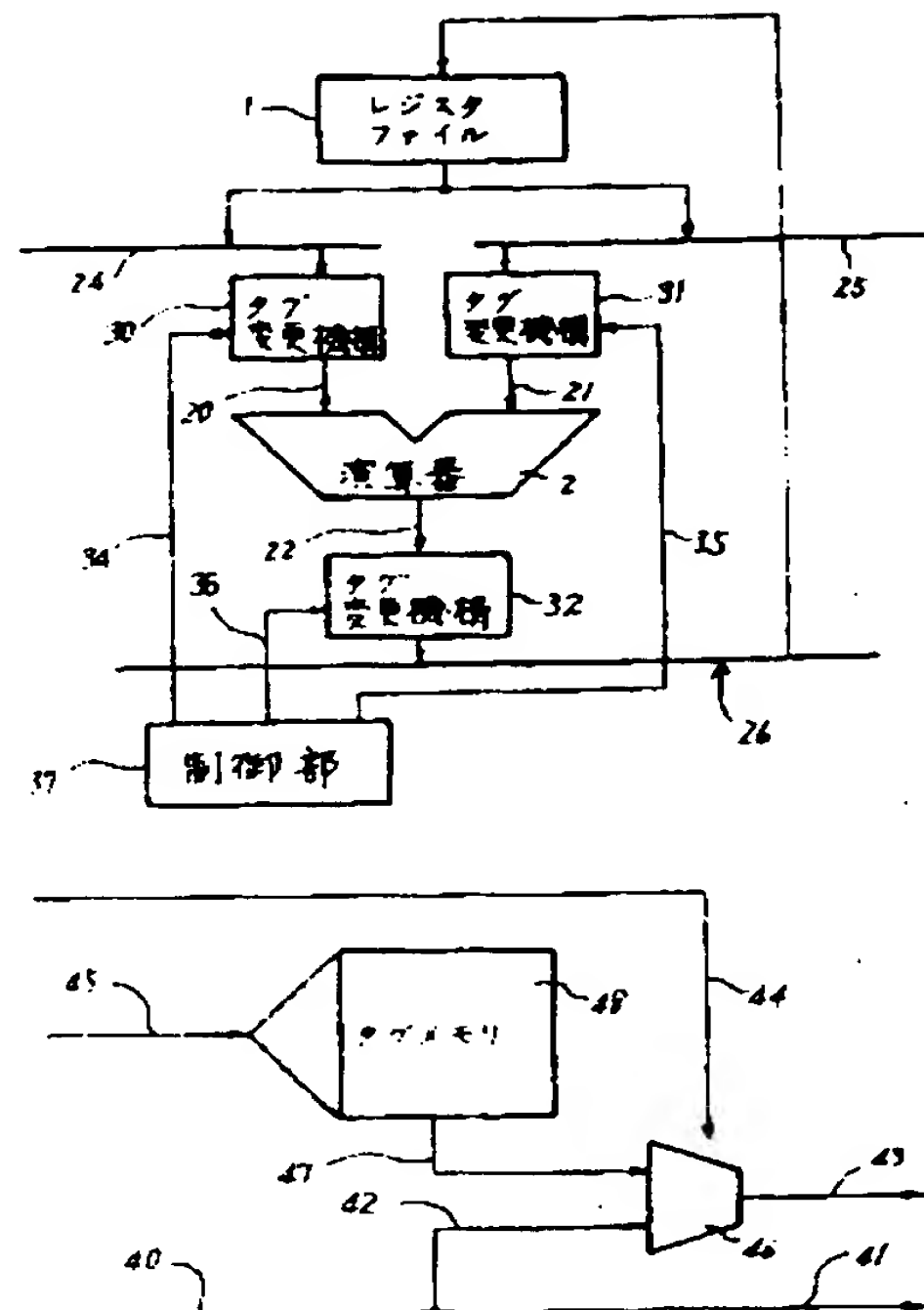
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TITLE : PROCESSOR OF TAGGED DATA



**ABSTRACT :** **PURPOSE:** To improve the processing speed of tagged data, by connecting a mechanism which changes tags in data in accordance with tag value designating inputs with an operator.

**CONSTITUTION:** An arithmetic control section designates one or two sets of data from a register file 1 composed of plural data holding registers and supplies the designated data to the input terminal 20 or 21 of an operator 2 through buses 24 and 25 and causes the operator 2 to execute prescribed operation on the data. After the operation, the control section stores the operated data in one register of the register file 1 from the output terminal 22 of the operator 2 through a bus 26. The input terminals 20 and 21 and output terminal 22 of the operator 2 are respectively connected with tag changing mechanisms 30-32. The tag changing mechanisms 30-32 can change tags constituted by means of specific bit position in each data on passing data. Necessity of tag change, tag value to be changed and set, etc., are controlled by signals inputted from control lines 34-36.

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